



SPEC CPU2006 SPECfp_rate power consumption on Dell, HP, and IBM servers

Executive summary

Dell Inc. (Dell) commissioned Principled Technologies (PT) to measure the SPEC CPU2006 SPECfp_rate2006 performance of the following four systems:

- Dell PowerEdge 2970 with two Dual-Core AMD Opteron 2222 SE processors
- Dell PowerEdge 2970 Energy Smart with two Dual-Core AMD Opteron 2212 HE processors
- HP ProLiant DL380 G5 with two Dual-Core Intel Xeon 5160 processors
- IBM System x3650 with two Dual-Core Intel Xeon 5160 processors

SPEC CPU2006 is an industry-standard benchmark created by the Standard Performance Evaluation Corp. (SPEC) to measure a server's compute-intensive performance. The benchmark consequently stresses the CPU and memory subsystems of the system under test. (For more information on SPEC CPU2006 and other SPEC benchmarks, see www.spec.org.)

The SPEC CPU2006 benchmark consists of two benchmark suites, each of which focuses on a different aspect of compute-intensive performance. CINT2006 measures and compares compute-intensive integer performance, while CFP2006 measures and compares compute-intensive floating-point performance. A "rate" version of each, which runs multiple instances of the benchmark to assess server throughput, is also available. We ran only the CFP2006 SPECfp_rate benchmark.

KEY FINDINGS

- The Dell PowerEdge 2970 Energy Smart delivered the highest performance per watt of the systems we tested during the SPECfp_rate2006 test (see Figure 1).
- The Dell PowerEdge 2970 and the Dell PowerEdge 2970 Energy Smart delivered 22.4 percent and 29.9 percent greater performance per watt, respectively, than the HP ProLiant DL380 G5 during the SPECfp_rate2006 test (see Figure 1).
- The Dell PowerEdge 2970 and the Dell PowerEdge 2970 Energy Smart delivered 12.9 percent and 19.8 percent greater performance per watt, respectively, than IBM System x3650 during the SPECfp_rate2006 test (see Figure 1).

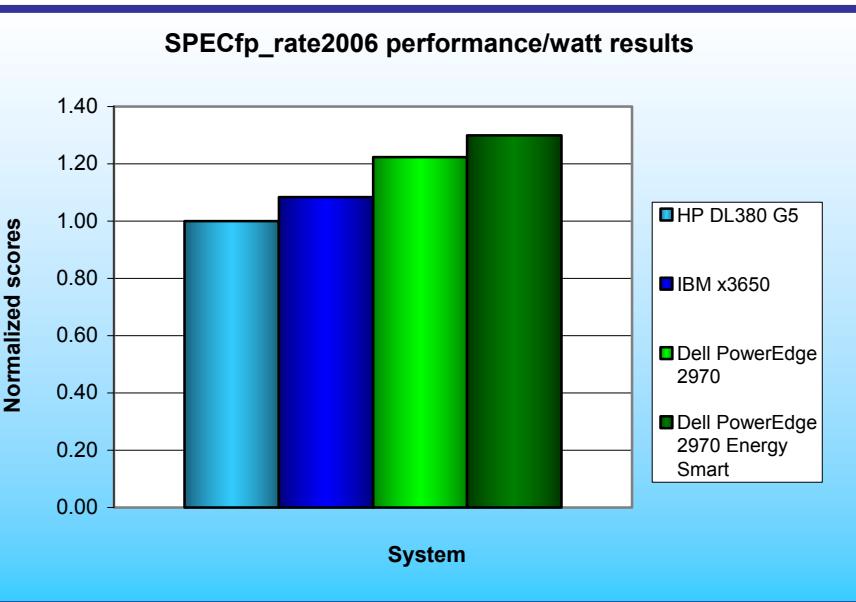


Figure 1: Normalized performance/watt results of the test servers. Higher numbers are better.

Figure 1 illustrates the relative performance/watt for each of the four servers normalized to that of the poorest-performing server. Both the Dell PowerEdge 2970 and the Dell PowerEdge 2970 Energy Smart delivered greater performance per watt than the HP ProLiant DL380 G5 and the IBM System x3650. The Dell PowerEdge 2970 and the Dell PowerEdge 2970 Energy Smart delivered 22.4 percent and 29.9 percent greater performance per watt, respectively, than the HP ProLiant DL380 G5. The Dell PowerEdge 2970 and the Dell PowerEdge 2970 Energy Smart delivered 12.9 percent and 19.8 percent greater performance per watt, respectively, than the IBM System x3650.

To calculate the performance/watt we used the following formula:

$$\frac{\text{benchmark score}}{\text{average power consumption in watts during period of peak performance}}$$

Workload

The SPEC CPU2006 workload includes two benchmark suites: CINT2006 and CFP2006. We ran only the CFP2006 benchmark, which focuses on measuring and comparing compute-intensive floating-point performance. Specifically, we measured the SPECfp_rate2006 results for the test servers with four users.

Figure 2 lists the 17 applications that compose the CFP2006 benchmark. SPEC wrote six of the applications using Fortran, three using C, four using both Fortran and C, and four using C++.

Name	Application area
410.bwaves	Fluid dynamics
416.gamess	Quantum chemistry
433.mic	Physics/Quantum chromodynamics
434.zeusmp	Physics/CFD
435.gromacs	Biochemistry/Molecular dynamics
436.cactusADM	Physics/General relativity
437.leslie3d	Fluid Dynamics
444.namd	Biology/Molecular dynamics
447.dealII	Finite element analysis
450.soplex	Linear programming, Optimization
453.povray	Image ray-tracing
454.calculix	Structural mechanics
459.GemsFDTD	Computational electromagnetics
465.tonto	Quantum chemistry
470.IBM	Fluid dynamics
481.wrf	Weather
482.sphinx3	Speech recognition

Figure 2: The applications that make up the CFP2006 benchmark.

A CFP2006 run performs each of the 17 application (tasks) three times and reports the median for each. It also calculates the geometric mean of those 17 results to produce an overall score.

Test results

Figure 4 details the results of our tests with four users for SPECfp_rate2006. We determined the number of users based on the number of execution units in a given server. We used the same number of SPECfp_rate2006 users as processor execution units, so there is a one-to-one ratio.

SPECfp_rate2006 performs three runs of each benchmark in the test suite and records the median, so the final score is a median of three runs. Higher scores are better.

Figure 3 details the power consumption, in watts, of the test servers while idle and during the benchmark. The idle power is an average of a two-minute power recording while the server was idle. The average power is an average power for the duration of the benchmark run.

Server	SPECfp_rate2006 results	SPECfp_base_rate2006 results	Idle power	Average power
Dell PowerEdge 2970	51.6	48.7	296.78	392.50
Dell PowerEdge 2970 Energy Smart	39.8	37.7	183.33	287.01
HP DL380 G5	42.6	41.3	302.00	396.50
IBM x3650	41.2	39.8	259.37	355.35

Figure 3: Power consumption, in watts, of the test servers while idle and during the benchmark.

Test methodology

Figure 4 summarizes some of the key aspects of the configurations of the server systems; Appendix A provides detailed configuration information.

Server	Dell PowerEdge 2970 with two Dual-Core AMD Opteron 2222 SE processors	Dell PowerEdge 2970 Energy Smart with two Dual-Core AMD Opteron 2212 HE processors	HP ProLiant DL380 G5 with two Dual-Core Intel Xeon 5160 processors	IBM System x3650 with two Dual-Core Intel Xeon 5160 processors
Processor frequency (GHz)	3.0 GHz	2.0 GHz	3.0 GHz	3.0 GHz
Front-side bus frequency (MHz)	1,000 MHz	1,000 MHz	1,333 MHz	1,333 MHz
Number of processor packages	2	2	2	2
Number of cores per processor package	2	2	2	2
Number of hardware threads per core	1	1	1	1
Motherboard	Dell CN-OFP973-69702-74H-0091	Dell CN-OFP973-69702-74H-0091	HP P3021ADMQUN203	IBM 7979AC1
Chipset	Broadcom HT-2100 and HT-1000 Chipset	Broadcom HT-2100 and HT-1000 Chipset	Intel 5000 Series Chipset	Intel 5000 Series Chipset
RAM (16 GB in each)	16 GB (8 x 2GB) PC2-5300 DDR2	16 GB (8 x 2GB) PC2-5300 DDR2	16 GB (8 x 2GB) PC2-5300 FBDIMM	16 GB (8 x 2GB) PC2-5300 FBDIMM
Hard drive	5 x 73GB Seagate ST973451SS	5 x 73GB Seagate ST973451SS	5 x 73GB Seagate ST973451SS	5 x 73GB Seagate ST973451SS

Figure 4: Summary of some key aspects of the server configurations.

Dell configured and provided all of the systems.

With the exception of disabling the Hardware Prefetcher and Adjacent Sector Prefetcher settings on the Intel-based servers, we used the default BIOS settings on each server. We changed those two settings to match those on similar systems with published SPEC CPU2006 results.

For all systems we used the default power settings, which were the following:

- Dell PowerEdge 2970—Demand-Based Power Management (PowerNow) disabled

- Dell PowerEdge 2970 Energy Smart—Demand-Based Power Management (PowerNow) enabled
- HP ProLiant DL380 G5—HP Dynamic Power Savings Mode enabled
- IBM System x3650—PowerExecutive Power Capping enabled

Changing those settings would have resulted in different power usage characteristics.

We began by installing a fresh copy of SuSE Linux Enterprise Server 10 SP1. We made no additional changes to the default installation options.

SPECCPU2006 configuration

We followed SPEC's standard instructions for building the CFP2006 executables. After studying the best results for this benchmark on the SPEC Web site, we chose the following software tools:

- HP ProLiant DL380 G5 and IBM System x3650
 - Intel C/C++ Compiler 9.1.051 for EM64T
 - Intel Fortran Compiler 9.1.051 for EM64T
- Dell PowerEdge 2970 and Dell PowerEdge 2970 Energy Smart
 - PathScale 3.0 C Compiler
 - PathScale 3.0 C++ Compiler
 - PathScale 3.0 Fortran Compiler

The benchmark requires configuration files. From the SPEC Web site we chose the most recent (as of the testing for this report) SPECCPU2006 results that used the above compiler. We copied the configuration files for those results and used them, with modifications to reflect the appropriate system information about the server under test, in our testing. The configuration files we used appear in Appendix B.

To begin the benchmark, we performed the following steps:

- Open a command prompt.
- Change to the cpu2006 directory.
- Type “`./shrc`” at the command prompt.
- Type “`ulimit -s unlimited`” at the command prompt.
- Enter “`runspec -c <config file name> -r 4 -T all -v 10 fp`” where
 - `<config file name>` = name of the configuration file

When the run completes, the benchmark puts the results in the directory `\cpu2006\result`. The result file names are of the form `CFP2006.<number>.<suffix>`. The suffixes are html, asc, raw, and pdf. The number is three digits and associates a result file with its log, e.g. `CFP2006.002. asc` and `log.002`.

Power measurement procedure

To record each server's power consumption during each test, we used an Extech* Instruments (www.extech.com) 380803 Power Analyzer/Datalogger. We connected the power cord from the server under test to the Power Analyzer's output load power outlet. We then plugged the power cord from the Power Analyzer's input voltage connection into a power outlet.

We used the Power Analyzer's Data Acquisition Software (version 3.0) to capture all recordings. We installed the software on a separate Intel-processor-based PC, which we connected to the Power Analyzer via an RS-232 cable. We captured power consumption at one-second intervals.

To gauge the idle power usage, we recorded the power usage for two minutes while each server was running the operating system but otherwise idle.

We then recorded the power usage (in watts) for each server during the testing at one-second intervals. To compute the average power usage, we averaged the power usage during the time the server was producing its peak performance results. We call this time the power measurement interval. See Figure 4 (idle and average peak power) for the results of these measurements.

Appendix A – Test system configuration information

This appendix provides detailed configuration information about each of the test server systems, which we list in alphabetical order.

Systems	Dell PowerEdge 2970 with two Dual-Core AMD Opteron 2222 SE processors	Dell PowerEdge 2970 Energy Smart with two Dual-Core AMD Opteron 2212 HE processors	HP ProLiant DL380 G5 with two Dual-Core Intel Xeon 5160 processors	IBM System x3650 with two Dual-Core Intel Xeon 5160 processors
General processor setup				
Number of processor packages	2	2	2	2
Number of cores per processor package	2	2	2	2
Number of hardware threads per core	1	1	1	1
CPU				
Vendor	AMD	AMD	Intel	Intel
Name	Opteron 2222 SE	Opteron 2212 HE	Xeon 5160	Xeon 5160
Stepping	F2	F2	B2	B2
Socket type	Socket F	Socket F	LGA771	LGA771
Core frequency (GHz)	3.0 GHz	2.0 GHz	3.0 GHz	3.0 GHz
Front-side bus frequency (MHz)	1,000 MHz HyperTransport technology	1,000 MHz HyperTransport technology	1,333 MHz	1,333 MHz
L1 cache	64 KB + 64 KB (per core)	64 KB + 64 KB (per core)	32 KB + 32KB (per core)	32 KB + 32KB (per core)
L2 cache	2 x 1 MB	2 x 1 MB	4 MB (shared)	4 MB (shared)
Platform				
Vendor and model number	Dual-Core AMD Opteron processor model 2222 SE-based server	Dual-Core AMD Opteron processor model 2212 HE-based server	Dual-Core Intel Xeon processor model 5160-based server	Dual-Core Intel Xeon processor model 5160-based server
Motherboard model number	Dell CN-OFP973-69702-74H-0091	Dell CN-OFP973-69702-74H-0091	HP P3021ADMQUN2 03	IBM 7979AC1
Motherboard chipset	Broadcom HT-2100 and HT-1000 Chipset	Broadcom HT-2100 and HT-1000 Chipset	Intel 5000 Series Chipset	Intel 5000 Series Chipset
Motherboard serial number	F3G57D1	F3G57D1	013096-001	KQHRHY0
BIOS name and version	Dell BIOS 1.0.0	Dell BIOS 1.0.0	HP BIOS P56	IBM BIOS 1.05
BIOS settings	Default	Default	Default	Default
Power settings (default)	Demand-Based Power Management (PowerNow) disabled	Demand-Based Power Management (PowerNow) enabled	HP Dynamic Power Savings Mode enabled	PowerExecutive Power Capping enabled

Systems	Dell PowerEdge 2970 with two Dual-Core AMD Opteron 2222 SE processors	Dell PowerEdge 2970 Energy Smart with two Dual-Core AMD Opteron 2212 HE processors	HP ProLiant DL380 G5 with two Dual-Core Intel Xeon 5160 processors	IBM System x3650 with two Dual-Core Intel Xeon 5160 processors
Memory module(s)				
Vendor and model number	Infineon HYS72T256220H P-3S-A	Infineon HYS72T256220H P-3S-A	Kingston KTH-XW667/4G	Kingston KTM5780/4G
Type	PC2-5300 DDR2	PC2-5300 DDR2	PC2-5300 FBDIMM	PC2-5300 FBDIMM
Speed (MHz)	667 MHz	667 MHz	667 MHz	667 MHz
Speed in the system currently running @ (MHz)	333 MHz	333 MHz	333 MHz	333 MHz
Timing/Latency (tCL-tRCD-iRP-tRASmin)	5-5-5-12	5-5-5-12	5-5-5-10	5-5-5-11
Size	16 GB	16 GB	16 GB	16 GB
Number of RAM modules	8 x 2,048 MB	8 x 2,048 MB	8 x 2,048 MB	8 x 2,048 MB
Chip organization	Double-sided	Double-sided	Double-sided	Double-sided
Hard disk				
Vendor and model number	Seagate ST973451SS	Seagate ST973451SS	Seagate ST973451SS	Seagate ST973451SS
Number of disks in system	5	5	5	5
Size	73 GB	73 GB	73 GB	73 GB
Buffer size	16 MB	16 MB	16 MB	16 MB
RPM	15,000	15,000	15,000	15,000
Type	SAS	SAS	SAS	SAS
Controller	PERC 5i	PERC 5i	Smart Array P400	IBM ServeRAID 8K
Remote management	Dell Remote Access Controller – N/A	Dell Remote Access Controller – N/A	iLO2 Standard Management – N/A	N/A
Power supplies				
Total number	2	2	2	1
Wattage of each	750	750	900	835

Figure 5: Detailed system configuration information for the test servers.

Appendix B – SPECfp_rate2006 configuration files

This appendix contains the benchmark configuration files we used to test the servers.

AMD processor based servers

```
#####
# AMD64 (64-bit) Linux Pathscale v3.0 config file
# for CPU2006
#####

flagsurl=http://www.spec.org/cpu2006/flags/CPU2006_flags.20070501.xml
tune      = base
ext       = amd514K8
output_format = asc.html
teeout    = yes
teerunout = yes
mean_anyway = yes
reportable = yes
sw_avail   = Sep-2007

submit           = taskset -c $SPECOPYNUM $command

sw_compiler000 = QLogic PathScale
sw_compiler001 = Compiler Suite, Release 3.0
sw_other000   = SmartHeap 8.1 32 bit Library for Linux
sw_auto_parallel = No

#####
# Macro section
#####
# Modify this section to use the appropriate architecture flags.
# Leave these commented out if you are defining them from the
# runspec command.

#define gnu4_fe
# #define gnu3_fe

default=default=default:
#####
# Compiler selection
#
#
CC      = pathcc
CXX     = pathCC
FC      = pathf95
SMARTHEAP_DIR = /cpu2006/mpaton/1.0/amd514K8.lib/32

#####
# Portability for default 64-bit code generation
#####

default=default=default:
PORTABILITY = -DSPEC_CPU_LP64

#####
# INT Portability
#####

400.perlbench=default=default:
CPORTABILITY = -DSPEC_CPU_LINUX_X64 -DSPEC_CPU_LP64

462.libquantum=default=default:
CPORTABILITY= -DSPEC_CPU_LINUX

471.omnetpp=default=default:
# Needed to avoid -DSPEC_CPU_LP64 on -m32 C++ base codes
```

```

PORTABILITY =

473.astar=default=default:
# Needed to avoid -DSPEC_CPU_LP64 on -m32 C++ base codes
PORTABILITY =

483.xalancbmk=default=default:
CXXPORTABILITY= -DSPEC_CPU_LINUX
# Needed to avoid -DSPEC_CPU_LP64 on -m32 C++ base codes
PORTABILITY =

#####
#          SPECint Tuning & Notes
#
#####

int=base:
COPTIMIZE     = -Ofast -OPT:malloc_alg=1
CXXOPTIMIZE   = -Ofast -m32
EXTRA_CXXLIBS= -L$(SMARTHEAP_DIR) -lsmartheap

sw_base_ptrsize  = 32/64-bit
sw_peak_ptrsize = 32/64-bit

#####
# INT Peak Tuning
#####

int=peak=default:
COPTIMIZE     = -Ofast -OPT:malloc_alg=1
CXXOPTIMIZE   = -Ofast -m32
EXTRA_CXXLIBS= -L$(SMARTHEAP_DIR) -lsmartheap
PASS1_CFLAGS   = -fb_create fbdata
PASS1_CXXFLAGS = -fb_create fbdata
PASS1_LDFLAGS  = -fb_opt fbdata
PASS2_CFLAGS   = -fb_opt fbdata
PASS2_CXXFLAGS = -fb_opt fbdata
PASS2_LDFLAGS  = -fb_opt fbdata

400.perlbench=peak=default:
COPTIMIZE=-Ofast -LNO:opt=0

401.bzip2=peak=default:
COPTIMIZE=-O3 -LNO:ou_prod_max=10 -OPT:Ofast:alias=disjoint
feedback=0

403.gcc=peak=default:
PORTABILITY =
COPTIMIZE=-m32 -O3 -OPT:Ofast

429.mcf=peak=default:
PORTABILITY =
COPTIMIZE=-m32 -O3 -ipa
feedback=0
EXTRA_CLIBS = -L$(SMARTHEAP_DIR) -lsmartheap

445.gobmk=peak=default:
COPTIMIZE=-O3 -OPT:alias=disjoint -LNO:simd=0:minvariant=off -WOPT:retype_expr=on

456.hmmr=peak=default:
COPTIMIZE=-O2 -OPT:alias=disjoint:malloc_alg=1 -CG:cflow=0
feedback=0

458.sjeng=peak=default:
COPTIMIZE=-O3 -IPA:plimit=50000 -IPA:pu_reordered=2

462.libquantum=peak=default:
COPTIMIZE=-O3 -ipa -CG:local_fwd_sched=on -IPA:space=1000
feedback=0

```

```

464.h264ref=peak=default:
COPTIMIZE=-O3 -IPA:plimit=20000 -OPT:alias=disjoint -LNO:prefetch=0

471.omnetpp=peak=default:
CXXOPTIMIZE= -Ofast -CG:gcm=off -m32
feedback=0

473.astar=peak=default:
basepeak=true

483.xalancbmk=peak=default:
CXXOPTIMIZE=-Ofast -m32 -OPT:unroll_times_max=8
feedback=0

#####
# FP Portability
#####

436.cactusADM=default=default:
FPORTABILITY= -fno-second-underscore

%ifdef %{gnu4_fe}
447.dealII=default=default:
CXXPORTABILITY =
%else
447.dealII=default=default:
CXXPORTABILITY = -DSPEC_CPU_TABLE_WORKAROUND
%endif

481.wrf=default=default:
FPORTABILITY= -fno-second-underscore
CPORTABILITY= -DSPEC_CPU_LINUX

#####
#           SPECfp Tuning
#
#####


fp=base:
COPTIMIZE      = -Ofast
CXXOPTIMIZE   = -Ofast
FOPTIMIZE     = -Ofast -OPT:malloc_alg=1

sw_base_ptrsize  = 64-bit
sw_peak_ptrsize = 32/64-bit
sw_other001= None

#####
# FP Peak Tuning
#####

fp=peak=default:
COPTIMIZE      = -Ofast
CXXOPTIMIZE   = -Ofast
FOPTIMIZE     = -Ofast -OPT:malloc_alg=1
PASS1_CFLAGS  = -fb_create fbdata
PASS2_CFLAGS  = -fb_opt fbdata
PASS1_CXXFLAGS= -fb_create fbdata
PASS2_CXXFLAGS= -fb_opt fbdata
PASS1_FFLAGS  = -fb_create fbdata
PASS2_FFLAGS  = -fb_opt fbdata
PASS1_LDFLAGS = -fb_create fbdata
PASS2_LDFLAGS = -fb_opt fbdata

410.bwaves=peak=default:
FOPTIMIZE=-O3 -OPT:Ofast:IEEE_arith=3 -LNO:blocking=off:ignore_feedback=off

416.gamess=peak=default:

```

```

FOPTIMIZE=-O2 -OPT:Ofast:ro=3:unroll_size=256

433.milc=peak=default:
COPTIMIZE=-Ofast -CG:cflow=off -LNO:prefetch=1 -OPT:malloc_alg=1
feedback=0

434.zeusmp=peak=default:
FOPTIMIZE=-Ofast -CG:local_fwd_sched=on -LNO:blocking=off:interchange=off:fu=10:full_unroll_outer=on
feedback=0

435.gromacs=peak=default:
FOPTIMIZE=-O3 -OPT:rsqrt=2:ro=3
COPTIMIZE=-O3 -OPT:rsqrt=2:ro=3
feedback=0

436.cactusADM=peak=default:
COPTIMIZE=-O3 -LNO:prefetch=3:prefetch_ahead=5:ou_prod_max=10:full_unroll=5 -ipa
FOPTIMIZE=-O3 -LNO:prefetch=3:prefetch_ahead=5:ou_prod_max=10:full_unroll=5 -ipa

437.leslie3d=peak=default:
basepeak=true

444.namd=peak=default:
CXXOPTIMIZE=-Ofast -fno-exceptions

447.dealII=peak=default:
# Needed to avoid -DSPEC_CPU_LP64
PORTABILITY =
#ifndef __gnu4_fe__
CXXOPTIMIZE=-Ofast -static -INLINE:aggressive=on -OPT:malloc_alg=1 -m32 -fno-exceptions
feedback=0
#else
CXXOPTIMIZE=-Ofast -INLINE:aggressive=on -LNO:opt=0 -OPT:alias=disjoint -m32 -fno-exceptions
feedback=0
#endif

450.soplex=peak=default:
CXXOPTIMIZE=-m32 -O3 -OPT:IEEE_arith=3 -CG:load_exe=0:movnti=1 -LNO:minvariant=off:prefetch=1 -fno-
exceptions
# Needed to avoid -DSPEC_CPU_LP64
PORTABILITY =

453.povray=peak=default:
CXXOPTIMIZE=-Ofast -fno-fast-math

454.calculix=peak=default:
FOPTIMIZE=-Ofast -LNO:simd=0 -WOPT:mem_opnds=on
COPTIMIZE=$ (FOPTIMIZE)
feedback=0

459.GemsFDTD=peak=default:
FOPTIMIZE=-Ofast -LNO:fission=2:prefetch=0
feedback=0

465.tonto=peak=default:
basepeak=true

470.lbm=peak=default:
basepeak=true

481.wrf=peak=default:
basepeak=true

482.sphinx3=peak=default:
#ifndef __gnu4_fe__
COPTIMIZE=-O3 -OPT:Ofast -WOPT:aggstr=0 -m32
#else
basepeak=true
#endif

```

Intel processor based servers

```
#####
# SPEC CPU2006 Intel SLES10 x64 (64-bit) config file #
# Intel Compiler 9.1 for Linux Intel64                 #
#####

action      = validate
tune        = base or peak
ext         = cpu2006.1.0.SLES10x64.ic91.em64t
PATHSEP     = /
flagsurl    = CPU2006_flags.20070417.xml

check_md5   = 1
mean_anyway = 1
reportable  = 1

default=default=default=default:
CC  = icc
CXX = icpc
FC  = ifort
OBJ = .o

SMARTHEAP_DIR = /opt/SmartHeap_8_1/lib

submit= MYMASK=`printf '0x%x' \$((1<<\$SPECCOPYNUM))` ; /usr/bin/taskset \$MYMASK $command

#####
# portability & libraries #
#####

fp=default=default=default:
PORTABILITY = -DSPEC_CPU_LP64

400.perlbench=default=default=default:
CPORTABILITY = -DSPEC_CPU_LINUX_X64

462.libquantum=default=default=default:
CPORTABILITY = -DSPEC_CPU_LINUX

483.xalancbmk=default=default=default:
CXXPORTABILITY = -DSPEC_CPU_LINUX

435.gromacs=default=default=default:
LDPORTABILITY = -nofor_main

436.cactusADM=default=default=default:
LDPORTABILITY = -nofor_main

454.calculix=default=default=default:
LDPORTABILITY = -nofor_main

481.wrf=default=default=default:
CPORTABILITY = -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

#####
# Baseline Tuning Flags #
#####

int=base=default=default:
COPTIMIZE= -fast
CXXOPTIMIZE= -xP -O3 -ipo -no-prec-div
EXTRA_CXXLIBS= -L$(SMARTHEAP_DIR) -lsmartheap

fp=base=default=default:
OPTIMIZE= -fast

#####
# Peak Tuning Flags #
#####
```

```

#####
int=peak=default=default:
OPTIMIZE= -fast
EXTRA_LIBS= -L$(SMARTHEAP_DIR) -lsmartheap

PASS1_CFLAGS      = -prof_gen
PASS2_CFLAGS      = -prof_use
PASS1_CXXFLAGS   = -prof_gen
PASS2_CXXFLAGS   = -prof_use
PASS1_LDFLAGS     = -prof_gen
PASS2_LDFLAGS     = -prof_use

fp=peak=default=default:
OPTIMIZE= -fast

PASS1_CFLAGS      = -prof_gen
PASS2_CFLAGS      = -prof_use
PASS1_FFLAGS      = -prof_gen
PASS2_FFLAGS      = -prof_use
PASS1_CXXFLAGS   = -prof_gen
PASS2_CXXFLAGS   = -prof_use
PASS1_LDFLAGS     = -prof_gen
PASS2_LDFLAGS     = -prof_use

400.perlbench=peak=default=default:
EXTRA_LIBS=

401.bzip2=peak=default=default:
CC=/opt/intel/cce/9.1.051/bin/icc -I/opt/intel/cce/9.1.051/include -L/opt/intel/cce/9.1.051/lib
PORTABILITY=-DSPEC_CPU_LP64
EXTRA_LIBS=
feedback=0

403.gcc=peak=default=default:
basepeak=yes

456.hmmmer=peak=default=default:
CC=/opt/intel/cce/9.1.051/bin/icc -I/opt/intel/cce/9.1.051/include -L/opt/intel/cce/9.1.051/lib
PORTABILITY=-DSPEC_CPU_LP64
EXTRA_LIBS=

462.libquantum=peak=default=default:
CC=/opt/intel/cce/9.1.051/bin/icc -I/opt/intel/cce/9.1.051/include -L/opt/intel/cce/9.1.051/lib
PORTABILITY=-DSPEC_CPU_LP64
EXTRA_LIBS=

471.omnetpp=peak=default=default:
OPTIMIZE=-xP -O3 -ipo -no-prec-div

483.xalancbmk=peak=default=default:
basepeak=yes

410.bwaves=peak=default=default:
basepeak=yes

416.gamess=peak=default=default:
basepeak=yes

433.milc=peak=default=default:
PORTABILITY=
CC=/opt/intel/cc/9.1.051/bin/icc -I/opt/intel/cc/9.1.051/include -L/opt/intel/cc/9.1.051/lib

434.zeusmp=peak=default=default:
PORTABILITY=
FC=/opt/intel/fc/9.1.051/bin/ifort -I/opt/intel/fc/9.1.051/include -L/opt/intel/fc/9.1.051/lib
feedback=0

436.cactusADM=peak=default=default:
basepeak=yes

```

```

437.leslie3d=peak=default=default:
basepeak=yes

444.namd=peak=default=default:
basepeak=yes

450.soplex=peak=default=default:
PORTABILITY=
CXX=/opt/intel/cc/9.1.051/bin/icpc -I/opt/intel/cc/9.1.051/include -L/opt/intel/cc/9.1.051/lib

459.GemsFDTD=peak=default=default:
basepeak=yes

470.lbm=peak=default=default:
PORTABILITY=
CC=/opt/intel/cc/9.1.051/bin/icc -I/opt/intel/cc/9.1.051/include -L/opt/intel/cc/9.1.051/lib

481.wrf=peak=default=default:
basepeak=yes

482.sphinx3=peak=default=default:
PORTABILITY=
CC=/opt/intel/cc/9.1.051/bin/icc -I/opt/intel/cc/9.1.051/include -L/opt/intel/cc/9.1.051/lib
feedback=0

#####
# Used Compilers and OS #
#####

int=default=default=default:
sw_compiler001 = Intel C++ Compiler for IA32/EM64T application,
sw_compiler002 = Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.051
sw_other       = Smart Heap Library, Version 8.1
sw_base_ptrsize = 32-bit
sw_peak_ptrsize = 32/64-bit

fp=default=default=default:
sw_compiler000 = Intel C++ Compiler for IA32/EM64T application,
sw_compiler001 = Version 9.1 - Build 20070215, Package-ID:
sw_compiler002 = l_cc_p_9.1.051
sw_compiler003 = Intel Fortran Compiler for IA32/EM64T application,
sw_compiler004 = Version 9.1 - Build 20070215, Package ID:
sw_compiler005 = l_fc_p_9.1.051
sw_other000    = SmartHeap 8.1 32 bit Library for Linux
sw_other001    = None
sw_base_ptrsize = 64-bit
sw_peak_ptrsize = 32/64-bit

default=default=default=default:
sw_os          = SuSE Linux Enterprise Server 10 SP1 64-bit kernel
sw_avail       = Sep-2007
sw_auto_parallel = No

```

Appendix C – SPECfp_rate2006 output

This appendix provides the output of the benchmark for each of the test servers.

Dell PowerEdge 2970 with two Dual-Core AMD Opteron 2222 SE processors

SPEC® CFP2006 Result																	
Dell Inc.					SPECfp®_rate2006 = 51.6												
PowerEdge 2970 AMD Opteron 2222 SE, 3.0 GHz					SPECfp_rate_base2006 = 48.7												
CPU2006 license #:	3184	Test sponsor:	Dell Inc.	Test date:	Sep-2007	Hardware Availability:	Sep-2007	Software Availability:	Sep-2007								
Tested by:	Principled Technologies																
410.bwaves	4	Copies	0 3.00 6.00 9.00 12.0 15.0 18.0 21.0 24.0 27.0 30.0 33.0 36.0 39.0 42.0 45.0 48.0 51.0 54.0 57.0 60.0 63.0 66.0 69.0 72.0 75.0 78.0 81.0 84.0 87.0 90.0 93.0 97.5			64.3											
416.gameSS	4					H			63.5								
433.milc	4								68.4								
434.zeusmp	4								60.1								
435.gromacs	4																
436.cactusADM	4																
437.leslie3d	4																
444.namd	4																
447.dealII	4																
450.soplex	4																
453.povray	4																
454.calculix	4																
459.GemsFDTD	4																
465.tonto	4																
470.lbm	4																
481.wrf	4																
482.sphinx3	4																
SPECfp_rate_base2006 = 48.7																	
SPECfp_rate2006 = 51.6																	
Hardware					Software												
CPU Name:	AMD Opteron 2222 SE				Operating System:	SuSE Linux Enterprise Server 10 SP1 64-bit kernel											
CPU Characteristics:	1000 MHz HyperTransport technology				Compiler:	QLogic PathScale											
CPU MHz:	3000				Compiler Suite:	Compiler Suite, Release 3.0											
FPU:	Integrated				Auto Parallel:	No											
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip				File System:	Linux/reiserfs											
CPU(s) orderable:	1,2 chips				System State:	Multi-user, run level 3											
Primary Cache:	64 KB I + 64 KB D on chip per core				Base Pointers:	64-bit											
Secondary Cache:	1 MB I+D on chip per core				Peak Pointers:	32/64-bit											
L3 Cache:	None				Other Software:	SmartHeap 8.1 32 bit Library for Linux											
Other Cache:	None					None											
Continued on next page																	
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Page 1																	

SPEC CFP2006 Result

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Dell Inc.

PowerEdge 2970
AMD Opteron 2222 SE, 3.0 GHz

CPU2006 license #: 3184 Test sponsor: Dell Inc. Test date: Sep-2007 Hardware Availability: Sep-2007 Software Availability: Sep-2007
Tested by: Principled Technologies

SPECfp_rate2006 = 51.6

SPECfp_rate_base2006 = 48.7

Hardware (Continued)	Memory: 16 GB (8x2 GB DDR2 PC2-5300)	Disk Subsystem: SAS, 73 GB	Other Hardware: None
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Results Table

Benchmark	Base						Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	854	63.7	<u>857</u>	<u>63.5</u>	862	63.0	4	<u>846</u>	<u>64.3</u>	846	64.2
416.gamess	4	1300	60.2	1310	59.9	<u>1300</u>	<u>60.1</u>	4	1140	68.5	<u>1150</u>	<u>68.4</u>
433.milc	4	960	38.2	<u>960</u>	<u>38.3</u>	958	38.3	4	967	38.0	960	38.2
434.zeusmp	4	775	47.0	<u>778</u>	<u>46.8</u>	781	46.6	4	775	47.0	771	47.2
435.gromacs	4	<u>534</u>	<u>53.4</u>	535	53.4	534	53.5	4	471	60.6	<u>471</u>	<u>60.7</u>
436.cactusADM	4	995	48.1	<u>991</u>	<u>48.2</u>	951	50.3	4	963	49.6	<u>954</u>	<u>50.1</u>
437.leslie3d	4	<u>959</u>	<u>39.2</u>	958	39.3	968	38.9	4	<u>959</u>	<u>39.2</u>	958	39.3
444.namd	4	<u>616</u>	<u>52.1</u>	616	52.0	616	52.1	4	<u>581</u>	<u>55.2</u>	581	55.2
447.dealII	4	611	74.9	<u>611</u>	<u>74.8</u>	618	74.1	4	477	95.9	<u>476</u>	<u>96.0</u>
450.soplex	4	<u>961</u>	<u>34.7</u>	964	34.6	960	34.8	4	849	39.3	848	39.3
453.povray	4	<u>307</u>	<u>69.3</u>	307	69.4	308	69.2	4	278	76.7	278	76.6
454.calculix	4	1040	31.7	<u>1040</u>	<u>31.7</u>	1040	31.7	4	<u>965</u>	<u>34.2</u>	964	34.2
459.GemsFDTD	4	<u>1230</u>	<u>34.5</u>	1220	34.7	1230	34.5	4	1200	35.2	<u>1200</u>	<u>35.3</u>
465.tonto	4	643	61.2	<u>644</u>	<u>61.1</u>	648	60.8	4	643	61.2	<u>644</u>	<u>61.1</u>
470.lbm	4	1490	36.8	1500	36.7	<u>1500</u>	<u>36.7</u>	4	1490	36.8	1500	36.7
481.wrf	4	778	57.4	<u>781</u>	<u>57.2</u>	789	56.7	4	778	57.4	<u>781</u>	<u>57.2</u>
482.sphinx3	4	<u>1450</u>	<u>53.6</u>	1460	53.6	1450	53.6	4	<u>1410</u>	<u>55.2</u>	1420	55.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation

C benchmarks:
pathcc

C++ benchmarks:
pathCC

Fortran benchmarks:
pathf95

Benchmarks using both Fortran and C:
pathcc pathf95

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SPEC CFP2006 Result					
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Dell Inc. PowerEdge 2970 AMD Opteron 2222 SE, 3.0 GHz			SPECfp_rate2006 = 51.6 SPECfp_rate_base2006 = 48.7		
CPU2006 license #: 3184	Test sponsor: Tested by:	Dell Inc. Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007
Base Portability Flags					
<p>C benchmarks: -DSPEC_CPU_LP64</p> <p>C++ benchmarks (except as noted below): -DSPEC_CPU_LP64</p> <p>453.povray: -DSPEC_CPU_LP64</p> <p>Fortran benchmarks: -DSPEC_CPU_LP64</p> <p>Benchmarks using both Fortran and C (except as noted below): -DSPEC_CPU_LP64</p> <p>436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore</p> <p>481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -fno-second-underscore</p>					
Peak Portability Flags					
<p>C benchmarks (except as noted below): -DSPEC_CPU_LP64</p> <p>C++ benchmarks (except as noted below): No flags used</p> <p>444.namd: -DSPEC_CPU_LP64</p> <p>453.povray: -DSPEC_CPU_LP64</p> <p>Fortran benchmarks (except as noted below): -DSPEC_CPU_LP64</p> <p>Benchmarks using both Fortran and C (except as noted below): -DSPEC_CPU_LP64</p> <p>436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore</p>					
Base Optimization Flags					
<p>C benchmarks: -Ofast</p> <p>C++ benchmarks: -Ofast</p> <p>Fortran benchmarks: -Ofast -OPT:malloc_alg=1</p>					
Continued on next page					
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SPEC CFP2006 Result

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Dell Inc.

PowerEdge 2970
AMD Opteron 2222 SE, 3.0 GHz

SPECfp_rate2006 = 51.6

SPECfp_rate_base2006 = 48.7

CPU2006 license #: 3184	Test sponsor: Tested by:	Dell Inc. Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007
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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-Ofast -OPT:malloc_alg=1`

Peak Optimization Flags

C benchmarks:

`433.milc: -Ofast -CG:cflow-off -LNO:prefetch=1 -OPT:malloc_alg=1`

`470.lbm: basepeak = yes`

`482.sphinx3: -O3 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -m32
-OPT:Ofast -WOPT:aggstr=0`

C++ benchmarks:

`444.namd: -Ofast -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-fno-exceptions`

`447.dealII: -Ofast -fno-exceptions -INLINE:aggressive-on -m32
-OPT:malloc_alg=1`

`450.soplex: -O3 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-fno-exceptions -CG:load_exe=0 -CG:movnti=1
-LNO:minvariant-off -LNO:prefetch=1 -m32 -OPT:IEEE_arith=3`

`453.povray: -Ofast -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-fno-fast-math`

Fortran benchmarks (except as noted below):
`basepeak = yes`

`410.bwaves: -O3 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-LNO:blocking-off -LNO:ignore_feedback-off -OPT:IEEE_arith=3
-OPT:Ofast`

`416.gamess: -O2 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256`

`434.zeusmp: -Ofast -CG:local_fwd_sched=on -LNO:blocking-off -LNO:fu=10
-LNO:full_unroll_outer=on -LNO:interchange-off`

`459.GemsFDTD: -Ofast -LNO:fission=2 -LNO:prefetch=0`

Benchmarks using both Fortran and C:

`435.gromacs: -O3 -OPT:ro=3 -OPT:rqqrt=2`

`436.cactusADM: -O3 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -ipa
-LNO:full_unroll=5 -LNO:ou_prod_max=10 -LNO:prefetch_ahead=5
-LNO:prefetch=3`

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SPEC CFP2006 Result					
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Dell Inc. PowerEdge 2970 AMD Opteron 2222 SE, 3.0 GHz			SPECfp_rate2006 = 51.6 SPECfp_rate_base2006 = 48.7		
CPU2006 license #: 3184 Test sponsor: Tested by:	Dell Inc. Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007	
Peak Optimization Flags (Continued)					
<pre>454.calculix: -Ofast -LNO:simd=0 -WOPT:mem_opnds=on 481.wrf:basepeak = yes</pre>					
Base Other Flags					
<p>C benchmarks: No flags used</p> <p>C++ benchmarks: No flags used</p> <p>Fortran benchmarks: No flags used</p> <p>Benchmarks using both Fortran and C: No flags used</p>					
Peak Other Flags					
<p>C benchmarks (except as noted below): No flags used</p> <p>C++ benchmarks (except as noted below): No flags used</p> <p>Fortran benchmarks (except as noted below): No flags used</p> <p>Benchmarks using both Fortran and C (except as noted below): No flags used</p>					
<p>SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.</p>					
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Dell PowerEdge 2970 Energy Smart with two Dual-Core AMD Opteron 2212 HE processors

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<table border="1"> <thead> <tr> <th>Cores</th> <th>1.00</th> <th>4.00</th> <th>8.00</th> <th>10.0</th> <th>12.0</th> <th>14.0</th> <th>16.0</th> <th>18.0</th> <th>20.0</th> <th>22.0</th> <th>24.0</th> <th>26.0</th> <th>28.0</th> <th>30.0</th> <th>32.0</th> <th>34.0</th> <th>36.0</th> <th>38.0</th> <th>40.0</th> <th>42.0</th> <th>44.0</th> <th>46.0</th> <th>48.0</th> <th>50.0</th> <th>52.0</th> <th>54.0</th> <th>56.0</th> <th>58.0</th> <th>60.0</th> <th>62.0</th> <th>64.0</th> <th>66.0</th> <th>68.0</th> </tr> </thead> <tbody> <tr> <td>410.bwaves</td> <td>4</td> <td></td> </tr> <tr> <td>416.gamess</td> <td>4</td> <td></td> </tr> <tr> <td>433.milc</td> <td>4</td> <td></td> </tr> <tr> <td>434.zeusmp</td> <td>4</td> <td></td> </tr> <tr> <td>435.gromacs</td> <td>4</td> <td></td> </tr> <tr> <td>436.cactusADM</td> <td>4</td> <td></td> </tr> <tr> <td>437.leslie3d</td> <td>4</td> <td></td> </tr> <tr> <td>444.namd</td> <td>4</td> <td></td> </tr> <tr> <td>447.dealII</td> <td>4</td> <td></td> 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MHz:</td><td>2000</td> <td>Auto Parallel:</td><td>Compiler Suite, Release 3.0</td> </tr> <tr> <td>FPU:</td><td>Integrated</td> <td>File System:</td><td>No</td> </tr> <tr> <td>CPU(s) enabled:</td><td>4 cores, 2 chips, 2 cores/chip</td> <td>System State:</td><td>Linux/reiserfs</td> </tr> <tr> <td>CPU(s) orderable:</td><td>1,2 chips</td> <td>Base Pointers:</td><td>Multi-user, run level 3</td> </tr> <tr> <td>Primary Cache:</td><td>64 KB I + 64 KB D on chip per core</td> <td>Peak Pointers:</td><td>64-bit</td> </tr> <tr> <td>Secondary Cache:</td><td>1 MB I+D on chip per core</td> <td>Other Software:</td><td>32/64-bit</td> </tr> <tr> <td>L3 Cache:</td><td>None</td> <td></td><td>SmartHeap 8.1 32 bit Library for Linux</td> </tr> <tr> <td>Other Cache:</td><td>None</td> <td></td><td>None</td> </tr> </tbody> </table> </td><td colspan="10"></td><td colspan="10"></td><td colspan="10"></td></tr> <tr> <td colspan="10">Continued on next page</td><td colspan="10"></td><td colspan="10"></td><td colspan="10"></td></tr> <tr> <td colspan="10">Standard Performance Evaluation Corporation info@spec.org http://www.spec.org/</td><td colspan="10">Page 1</td><td colspan="10"></td><td colspan="10"></td></tr> </tbody> </table>	Cores	1.00	4.00	8.00	10.0	12.0	14.0	16.0	18.0	20.0	22.0	24.0	26.0	28.0	30.0	32.0	34.0	36.0	38.0	40.0	42.0	44.0	46.0	48.0	50.0	52.0	54.0	56.0	58.0	60.0	62.0	64.0	66.0	68.0	410.bwaves	4																																										416.gamess	4																																										433.milc	4																																										434.zeusmp	4																																										435.gromacs	4																																										436.cactusADM	4																																										437.leslie3d	4																																										444.namd	4																																										447.dealII	4																																										450.soplex	4																																										453.povray	4																																										454.calculix	4																																										459.GemsFDTD	4																																										465.tonto	4																																										470.lbm	4																																										481.wrf	4																																										482.sphinx3	4																																										SPECfp_rate_base2006 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<td>Secondary Cache:</td><td>1 MB I+D on chip per core</td> <td>Other Software:</td><td>32/64-bit</td> </tr> <tr> <td>L3 Cache:</td><td>None</td> <td></td><td>SmartHeap 8.1 32 bit Library for Linux</td> </tr> <tr> <td>Other Cache:</td><td>None</td> <td></td><td>None</td> </tr> </tbody> </table>										Hardware		Software		CPU Name:	AMD Opteron 2212 HE	Operating System:	SuSE Linux Enterprise Server 10 SP1 64-bit kernel	CPU Characteristics:	1000 MHz HyperTransport technology	Compiler:	QLogic PathScale	CPU MHz:	2000	Auto Parallel:	Compiler Suite, Release 3.0	FPU:	Integrated	File System:	No	CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip	System State:	Linux/reiserfs	CPU(s) orderable:	1,2 chips	Base Pointers:	Multi-user, run level 3	Primary Cache:	64 KB I + 64 KB D on chip per core	Peak Pointers:	64-bit	Secondary Cache:	1 MB I+D on chip per core	Other Software:	32/64-bit	L3 Cache:	None		SmartHeap 8.1 32 bit Library for Linux	Other Cache:	None		None																															Continued on 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SPEC CFP2006 Result												
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Dell Inc.						SPECfp_rate2006 = 39.8						
PowerEdge 2970 Energy Smart AMD Opteron 2212 HE, 2.0 GHz						SPECfp_rate_base2006 = 37.7						
CPU2006 license #: 3184	Test sponsor:	Dell Inc.	Test date:	Sep-2007	Hardware Availability:	Sep-2007	Software Availability:	Sep-2007				
Tested by:	Principled Technologies											
Hardware (Continued)												
Memory:	16 GB (8x2 GB DDR2 PC2-5300)											
Disk Subsystem:	SAS, 73 GB											
Other Hardware:	None											
Results Table												
Benchmark	Base						Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Copies	Seconds	Ratio	Seconds	Ratio	
410.bwaves	4	1030	52.6	1030	52.6	1040	52.5	4	981	55.4	976	
416.gamess	4	1950	40.2	1950	40.1	1950	40.2	4	1720	45.7	1730	
433.milc	4	1080	33.9	1080	33.9	1080	33.9	4	1060	34.6	1060	
434.zeusmp	4	1030	35.3	1030	35.3	1030	35.3	4	1020	35.6	1020	
435.gromacs	4	793	36.0	790	36.1	790	36.2	4	695	41.1	699	
436.cactusADM	4	1270	37.7	1250	38.1	1250	38.3	4	1250	38.2	1240	
437.leslie3d	4	1110	33.9	1110	33.8	1120	33.6	4	1110	33.9	1110	
444.namd	4	923	34.7	921	34.8	921	34.8	4	870	36.9	869	
447.dealII	4	839	54.6	839	54.5	839	54.5	4	667	68.6	668	
450.soplex	4	1110	30.0	1110	30.0	1110	30.0	4	986	33.8	988	
453.povray	4	460	46.2	460	46.3	459	46.3	4	416	51.1	417	
454.calculix	4	1540	21.4	1550	21.4	1540	21.4	4	1430	23.1	1430	
459.GemsFDTD	4	1320	32.2	1330	32.0	1330	32.0	4	1320	32.1	1310	
465.tonto	4	892	44.1	887	44.4	890	44.2	4	892	44.1	887	
470.lbm	4	1600	34.3	1630	33.8	1590	34.5	4	1600	34.3	1630	
481.wrf	4	996	44.8	1000	44.6	999	44.7	4	996	44.8	1000	
482.sphinx3	4	1790	43.6	1780	43.8	1780	43.7	4	1780	43.9	1790	
Results appear in the order in which they were run. Bold underlined text indicates a median measurement.												
Compiler Invocation												
C benchmarks: pathcc												
C++ benchmarks: pathCC												
Fortran benchmarks: pathf95												
Benchmarks using both Fortran and C: pathcc pathf95												
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Page 2												

SPEC CFP2006 Result					
Copyright ©2006 Standard Performance Evaluation Corporation					
Dell Inc. PowerEdge 2970 Energy Smart AMD Opteron 2212 HE, 2.0 GHz			SPECfp_rate2006 = 39.8 SPECfp_rate_base2006 = 37.7		
CP02006 license #: 3184	Test sponsor: Tested by:	Dell Inc. Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007
Base Portability Flags					
<p>C benchmarks: -DSPEC_CPU_LP64</p> <p>C++ benchmarks (except as noted below): -DSPEC_CPU_LP64</p> <p>453.povray: -DSPEC_CPU_LP64</p> <p>Fortran benchmarks: -DSPEC_CPU_LP64</p> <p>Benchmarks using both Fortran and C (except as noted below): -DSPEC_CPU_LP64</p> <p>436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore</p> <p>481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -fno-second-underscore</p>					
Peak Portability Flags					
<p>C benchmarks (except as noted below): -DSPEC_CPU_LP64</p> <p>C++ benchmarks (except as noted below): No flags used</p> <p>444.namd: -DSPEC_CPU_LP64</p> <p>453.povray: -DSPEC_CPU_LP64</p> <p>Fortran benchmarks (except as noted below): -DSPEC_CPU_LP64</p> <p>Benchmarks using both Fortran and C (except as noted below): -DSPEC_CPU_LP64</p> <p>436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore</p>					
Base Optimization Flags					
<p>C benchmarks: -Ofast</p> <p>C++ benchmarks: -Ofast</p> <p>Fortran benchmarks: -Ofast -OPT:malloc_alg=1</p>					
Continued on next page					
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Page 3					

SPEC CFP2006 Result

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Dell Inc.

PowerEdge 2970 Energy Smart
AMD Opteron 2212 HE, 2.0 GHz

CPU2006 license #: 3184	Test sponsor: Tested by:	Dell Inc. Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007
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SPECfp_rate2006 = 39.8

SPECfp_rate_base2006 = 37.7

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-Ofast -OPT:malloc_alg-1

Peak Optimization Flags

C benchmarks:

433.milc: -Ofast -CG:cflow-off -LNO:prefetch-1 -OPT:malloc_alg-1

470.lbm: basepeak - yes

482.sphinx3: -O3 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -m32
-OPT:Ofast -WOPT:aggstr-0

C++ benchmarks:

444.namd: -Ofast -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-fno-exceptions

447.dealII: -Ofast -fno-exceptions -INLINE:aggressive-on -m32
-OPT:malloc_alg-1

450.soplex: -O3 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-fno-exceptions -CG:load_exe-0 -CG:movnti-1
-LNO:minvariant-off -LNO:prefetch-1 -m32 -OPT:IEEE_arith-3

453.povray: -Ofast -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-fno-fast-math

Fortran benchmarks (except as noted below):

basepeak - yes

410.bwaves: -O3 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-LNO:blocking-off -LNO:ignore_feedback-off -OPT:IEEE_arith-3
-OPT:Ofast

416.gamess: -O2 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2)
-OPT:Ofast -OPT:ro-3 -OPT:unroll_size-256

434.zeusmp: -Ofast -CG:local_fwd_sched-on -LNO:blocking-off -LNO:fu-10
-LNO:full_unroll_outer-on -LNO:interchange-off

459.GemsFDTD: -Ofast -LNO:fission-2 -LNO:prefetch-0

Benchmarks using both Fortran and C:

435.gromacs: -O3 -OPT:ro-3 -OPT:rqqrt-2

436.cactusADM: -O3 -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -ipa
-LNO:full_unroll-5 -LNO:ou_prod_max-10 -LNO:prefetch_ahead-5
-LNO:prefetch-3

Continued on next page

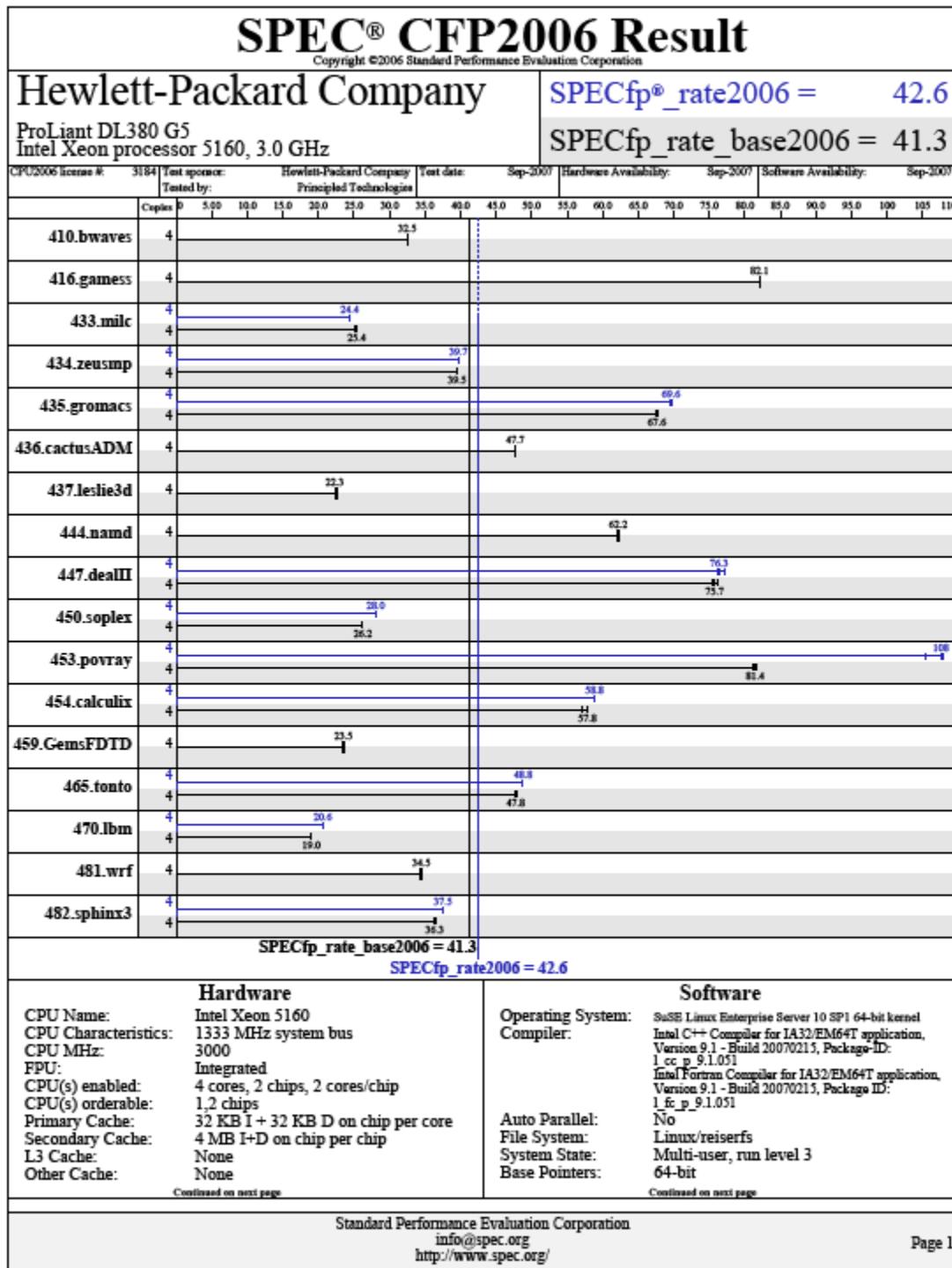
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SPEC CFP2006 Result					
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Dell Inc. PowerEdge 2970 Energy Smart AMD Opteron 2212 HE, 2.0 GHz			SPECfp_rate2006 = 39.8 SPECfp_rate_base2006 = 37.7		
CPU2006 license #: 3184	Test sponsor: Tested by:	Dell Inc. Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007
Peak Optimization Flags (Continued)					
<pre>454.calculix: -Ofast -LNO:simd=0 -WOPT:mem_opnds=on 481.wrf:basepeak = yes</pre>					
Base Other Flags					
<p>C benchmarks: No flags used</p> <p>C++ benchmarks: No flags used</p> <p>Fortran benchmarks: No flags used</p> <p>Benchmarks using both Fortran and C: No flags used</p>					
Peak Other Flags					
<p>C benchmarks (except as noted below): No flags used</p> <p>C++ benchmarks (except as noted below): No flags used</p> <p>Fortran benchmarks (except as noted below): No flags used</p> <p>Benchmarks using both Fortran and C (except as noted below): No flags used</p>					
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HP ProLiant DL380 G5 with two Dual-Core Intel Xeon 5160 processors



SPEC CFP2006 Result																
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Hewlett-Packard Company								SPECfp_rate2006 = 42.6								
ProLiant DL380 G5 Intel Xeon processor 5160, 3.0 GHz								SPECfp_rate_base2006 = 41.3								
CPU2006 license #:	3184	Test sponsor:	Hewlett-Packard Company	Test date:	Sep-2007	Hardware Availability:	Sep-2007	Software Availability:	Sep-2007							
Tested by:		Principled Technologies														
Hardware (Continued)								Software (Continued)								
Memory:	16 GB (8x2 GB DDR2 PC2-5300)							Peak Pointers:	32/64-bit							
Disk Subsystem:	SAS, 73 GB							Other Software:	SmartHeap 8.1 32 bit Library for Linux							
Other Hardware:	None							None								
Results Table																
Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1670	32.6	1670	32.5	<u>1670</u>	<u>32.5</u>	4	1670	32.6	1670	32.5	<u>1670</u>	<u>32.5</u>		
416.gamess	4	954	82.1	954	82.1	954	82.1	4	954	82.1	954	82.1	954	82.1		
433.milc	4	1440	25.4	1460	25.2	<u>1450</u>	<u>25.4</u>	4	<u>1510</u>	<u>24.4</u>	1510	24.4	1510	24.4		
434.zeusmp	4	922	39.5	922	39.5	922	39.5	4	917	39.7	917	39.7	917	39.7		
435.gromacs	4	422	67.6	422	67.7	<u>422</u>	<u>67.6</u>	4	410	69.6	410	69.6	411	69.5		
436.cactusADM	4	1000	47.7	1000	47.8	<u>1000</u>	<u>47.7</u>	4	1000	47.7	1000	47.8	<u>1000</u>	<u>47.7</u>		
437.leslie3d	4	1690	22.3	1660	22.6	1690	22.3	4	1690	22.3	1660	22.6	1690	22.3		
444.namd	4	515	62.3	516	62.2	517	62.1	4	515	62.3	516	62.2	517	62.1		
447.dealII	4	600	76.3	604	75.7	607	75.4	4	599	76.3	594	77.1	600	76.2		
450.soplex	4	1280	26.1	1270	26.2	1270	26.2	4	1190	28.1	1190	28.0	1190	28.0		
453.povray	4	261	81.4	261	81.6	262	81.1	4	198	108	202	105	197	108		
454.calculix	4	571	57.8	578	57.1	570	57.9	4	562	58.7	561	58.8	560	58.9		
459.GemsFDTD	4	1800	23.5	1800	23.5	1800	23.5	4	1800	23.5	1800	23.5	1800	23.5		
465.tonto	4	822	47.9	824	47.8	825	47.7	4	807	48.8	809	48.6	807	48.8		
470.lbm	4	2890	19.0	2890	19.0	2890	19.0	4	2670	20.6	2670	20.6	2670	20.6		
481.wrf	4	1300	34.3	1290	34.6	1300	34.5	4	1300	34.3	1290	34.6	1300	34.5		
482.sphinx3	4	2150	36.3	2140	36.3	2140	36.4	4	2070	37.6	2080	37.5	2080	37.5		
Results appear in the order in which they were run. Bold underlined text indicates a median measurement.																
General Notes																
BIOS configuration: Hardware Prefetch - Disable, Adjacent Sector Prefetch - Disable																
Base Compiler Invocation																
C benchmarks: icc																
C++ benchmarks: icpc																
Fortran benchmarks: ifort																
Continued on next page																
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SPEC CFP2006 Result					
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Hewlett-Packard Company			SPECfp_rate2006 = 42.6		
ProLiant DL380 G5 Intel Xeon processor 5160, 3.0 GHz			SPECfp_rate_base2006 = 41.3		
CPU2006 license #: 3184	Test sponsor: Hewlett-Packard Company Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007	
Tested by:					
Base Compiler Invocation (Continued)					
Benchmarks using both Fortran and C: ifort icc					
Peak Compiler Invocation					
C benchmarks: /opt/intel/cc/9.1.051/bin/icc -I/opt/intel/cc/9.1.051/include -L/opt/intel/cc/9.1.051/lib					
C++ benchmarks (except as noted below): icpc 450.soplex: /opt/intel/cc/9.1.051/bin/icpc -I/opt/intel/cc/9.1.051/include -L/opt/intel/cc/9.1.051/lib					
Fortran benchmarks: 434.zeusmp: /opt/intel/fc/9.1.051/bin/ifort -I/opt/intel/fc/9.1.051/include -L/opt/intel/fc/9.1.051/lib 465.tonto: ifort					
Benchmarks using both Fortran and C (except as noted below): ifort icc					
Base Portability Flags					
C benchmarks: -DSPEC_CPU_LP64					
C++ benchmarks (except as noted below): -DSPEC_CPU_LP64 453.povray: -DSPEC_CPU_LP64					
Fortran benchmarks: -DSPEC_CPU_LP64					
Benchmarks using both Fortran and C (except as noted below): -DSPEC_CPU_LP64 -nofor_main 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG					
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SPEC CFP2006 Result					
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Hewlett-Packard Company			SPECfp_rate2006 = 42.6		
ProLiant DL380 G5 Intel Xeon processor 5160, 3.0 GHz			SPECfp_rate_base2006 = 41.3		
CPU2006 license #: 3184	Test sponsor: Hewlett-Packard Company Tested by: Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007	
Peak Portability Flags					
<p>C benchmarks: No flags used</p> <p>C++ benchmarks:</p> <ul style="list-style-type: none"> 447.dealII: -DSPEC_CPU_LP64 450.soplex: No flags used 453.povray: -DSPEC_CPU_LP64 <p>Fortran benchmarks:</p> <ul style="list-style-type: none"> 434.zeusmp: No flags used 465.tonto: -DSPEC_CPU_LP64 <p>Benchmarks using both Fortran and C (except as noted below):</p> <ul style="list-style-type: none"> -DSPEC_CPU_LP64 -nofor_main 					
Base Optimization Flags					
<p>C benchmarks: -fast</p> <p>C++ benchmarks: -fast</p> <p>Fortran benchmarks: -fast</p> <p>Benchmarks using both Fortran and C:</p> <ul style="list-style-type: none"> -fast 					
Peak Optimization Flags					
<p>C benchmarks (except as noted below):</p> <ul style="list-style-type: none"> -fast -prof_gen(pass 1) -prof_use(pass 2) 482.sphinx3: -fast <p>C++ benchmarks (except as noted below):</p> <ul style="list-style-type: none"> -fast -prof_gen(pass 1) -prof_use(pass 2) 444.namd: basepeak = yes <p>Fortran benchmarks (except as noted below):</p> <ul style="list-style-type: none"> basepeak = yes 					
Continued on next page					
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SPEC CFP2006 Result					
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Hewlett-Packard Company			SPECfp_rate2006 = 42.6		
ProLiant DL380 G5 Intel Xeon processor 5160, 3.0 GHz			SPECfp_rate_base2006 = 41.3		
CPU2006 license #:	3184	Test sponsor:	Hewlett-Packard Company	Test date:	Sep-2007
Tested by:		Principled Technologies		Hardware Availability:	Sep-2007
Software Availability:				Software Availability:	Sep-2007
Peak Optimization Flags (Continued)					
434.zeusmp: -fast					
465.tonto: -fast -prof_gen(pass 1) -prof_use(pass 2)					
Benchmarks using both Fortran and C (except as noted below):					
-fast -prof_gen(pass 1) -prof_use(pass 2)					
436.cactusADM: basepeak - yes					
481.wrf: basepeak - yes					
Other Flags					
C benchmarks:					
No flags used					
C++ benchmarks:					
No flags used					
Fortran benchmarks:					
No flags used					
Benchmarks using both Fortran and C:					
No flags used					
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IBM System x3650 with two Dual-Core Intel Xeon 5160 processors

SPEC® CFP2006 Result																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
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<table border="1"> <thead> <tr> <th>Cores</th> <th>0</th> <th>5.0</th> <th>10.0</th> <th>15.0</th> <th>20.0</th> <th>25.0</th> <th>30.0</th> <th>35.0</th> <th>40.0</th> <th>45.0</th> <th>50.0</th> <th>55.0</th> <th>60.0</th> <th>65.0</th> <th>70.0</th> <th>75.0</th> <th>80.0</th> <th>85.0</th> <th>90.0</th> <th>95.0</th> <th>100</th> <th>105</th> <th>110</th> </tr> </thead> <tbody> <tr> <td>410.bwaves</td> <td>4</td> <td></td> </tr> <tr> <td>416.gamess</td> <td>4</td> <td></td> </tr> <tr> <td>433.milc</td> <td>4</td> <td></td> </tr> <tr> <td>434.zeusmp</td> <td>4</td> <td></td> </tr> <tr> <td>435.gromacs</td> <td>4</td> <td></td> </tr> <tr> <td>436.cactusADM</td> <td>4</td> <td></td> </tr> <tr> <td>437.leslie3d</td> <td>4</td> <td></td> </tr> <tr> <td>444.namd</td> <td>4</td> <td></td> </tr> <tr> <td>447.dealIII</td> <td>4</td> <td></td> </tr> <tr> <td>450.soplex</td> <td>4</td> <td></td> </tr> <tr> <td>453.povray</td> <td>4</td> <td></td> </tr> <tr> <td>454.calculix</td> <td>4</td> <td></td> </tr> <tr> <td>459.GemsFDTD</td> <td>4</td> <td></td> </tr> <tr> <td>465.tonto</td> <td>4</td> <td></td> </tr> <tr> <td>470.lbm</td> <td>4</td> <td></td> </tr> <tr> <td>481.wrf</td> <td>4</td> <td></td> </tr> <tr> <td>482.sphinx3</td> <td>4</td> <td></td> </tr> </tbody> </table>												Cores	0	5.0	10.0	15.0	20.0	25.0	30.0	35.0	40.0	45.0	50.0	55.0	60.0	65.0	70.0	75.0	80.0	85.0	90.0	95.0	100	105	110	410.bwaves	4																								416.gamess	4																								433.milc	4																								434.zeusmp	4																								435.gromacs	4																								436.cactusADM	4																								437.leslie3d	4																								444.namd	4																								447.dealIII	4																								450.soplex	4																								453.povray	4																								454.calculix	4																								459.GemsFDTD	4																								465.tonto	4																								470.lbm	4																								481.wrf	4																								482.sphinx3	4																								SPECfp_rate_base2006 = 39.8 SPECfp_rate2006 = 41.2					
Cores	0	5.0	10.0	15.0	20.0	25.0	30.0	35.0	40.0	45.0	50.0	55.0	60.0	65.0	70.0	75.0	80.0	85.0	90.0	95.0	100	105	110																																																																																																																																																																																																																																																																																																																																																																																																																																																											
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<table border="1"> <thead> <tr> <th colspan="2">Hardware</th> <th colspan="10">Software</th> </tr> </thead> <tbody> <tr> <td>CPU Name:</td> <td>Intel Xeon 5160</td> <td>Operating System:</td> <td colspan="9">SuSE Linux Enterprise Server 10 SP1 64-bit kernel</td> </tr> <tr> <td>CPU Characteristics:</td> <td>1333 MHz system bus</td> <td>Compiler:</td> <td colspan="9">Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: 1_cc_p_9.1.051</td> </tr> <tr> <td>CPU MHz:</td> <td>3000</td> <td></td> <td colspan="9">Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: 1_ft_p_9.1.051</td> </tr> <tr> <td>FPU:</td> <td>Integrated</td> <td>Auto Parallel:</td> <td colspan="9">No</td> </tr> <tr> <td>CPU(s) enabled:</td> <td>4 cores, 2 chips, 2 cores/chip</td> <td>File System:</td> <td colspan="9">Linux/reiserfs</td> </tr> <tr> <td>CPU(s) orderable:</td> <td>1,2 chips</td> <td>System State:</td> <td colspan="9">Multi-user, run level 3</td> </tr> <tr> <td>Primary Cache:</td> <td>32 KB I + 32 KB D on chip per core</td> <td>Base Pointers:</td> <td colspan="9">64-bit</td> </tr> <tr> <td>Secondary Cache:</td> <td>4 MB I+D on chip per chip</td> <td></td> <td colspan="9"></td> </tr> <tr> <td>L3 Cache:</td> <td>None</td> <td></td> <td colspan="9"></td> </tr> <tr> <td>Other Cache:</td> <td>None</td> <td></td> <td colspan="9"></td> </tr> </tbody> </table>												Hardware		Software										CPU Name:	Intel Xeon 5160	Operating System:	SuSE Linux Enterprise Server 10 SP1 64-bit kernel									CPU Characteristics:	1333 MHz system bus	Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: 1_cc_p_9.1.051									CPU MHz:	3000		Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: 1_ft_p_9.1.051									FPU:	Integrated	Auto Parallel:	No									CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip	File System:	Linux/reiserfs									CPU(s) orderable:	1,2 chips	System State:	Multi-user, run level 3									Primary Cache:	32 KB I + 32 KB D on chip per core	Base Pointers:	64-bit									Secondary Cache:	4 MB I+D on chip per chip											L3 Cache:	None											Other Cache:	None											Continued on next page																																																																																																																																																																																																																																																																																																																																		
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CPU MHz:	3000		Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: 1_ft_p_9.1.051																																																																																																																																																																																																																																																																																																																																																																																																																																																																															
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CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip	File System:	Linux/reiserfs																																																																																																																																																																																																																																																																																																																																																																																																																																																																															
CPU(s) orderable:	1,2 chips	System State:	Multi-user, run level 3																																																																																																																																																																																																																																																																																																																																																																																																																																																																															
Primary Cache:	32 KB I + 32 KB D on chip per core	Base Pointers:	64-bit																																																																																																																																																																																																																																																																																																																																																																																																																																																																															
Secondary Cache:	4 MB I+D on chip per chip																																																																																																																																																																																																																																																																																																																																																																																																																																																																																	
L3 Cache:	None																																																																																																																																																																																																																																																																																																																																																																																																																																																																																	
Other Cache:	None																																																																																																																																																																																																																																																																																																																																																																																																																																																																																	
Standard Performance Evaluation Corporation info@spec.org http://www.spec.org/												Page 1																																																																																																																																																																																																																																																																																																																																																																																																																																																																						

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IBM Corporation							SPECfp_rate2006 = 41.2									
IBM System x3650 Intel Xeon processor 5160, 3.0 GHz							SPECfp_rate_base2006 = 39.8									
CPU2006 license #:	3184	Test sponsor:	IBM Corporation	Test date:	Sep-2007	Hardware Availability:	Sep-2007	Software Availability:	Sep-2007							
Tested by:		Principled Technologies														
Hardware (Continued)							Software (Continued)									
Memory:	16 GB (8x2 GB DDR2 PC2-5300)			Peak Pointers: 32/64-bit			Other Software:	SmartHeap 8.1 32 bit Library for Linux								
Disk Subsystem:	SAS, 73 GB			None				None								
Results Table																
Benchmark	Base							Peak								
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	1830	29.8	<u>1780</u>	<u>30.5</u>	1780	30.5	4	1830	29.8	<u>1780</u>	<u>30.5</u>	1780	30.5		
416.gamess	4	955	82.0	<u>955</u>	<u>82.0</u>	955	82.0	4	955	82.0	<u>955</u>	<u>82.0</u>	955	82.0		
433.milc	4	1580	23.2	1500	24.5	<u>1500</u>	<u>24.5</u>	4	1550	23.7	1530	24.0	<u>1540</u>	<u>23.8</u>		
434.zeusmp	4	963	37.8	<u>953</u>	<u>38.2</u>	950	38.3	4	943	38.6	<u>943</u>	<u>38.6</u>	937	38.8		
435.gromacs	4	<u>425</u>	<u>67.3</u>	424	67.4	425	67.2	4	412	69.3	410	69.6	<u>411</u>	<u>69.5</u>		
436.cactusADM	4	1060	45.2	1030	46.3	<u>1040</u>	<u>46.0</u>	4	1060	45.2	1030	46.3	<u>1040</u>	<u>46.0</u>		
437.leslie3d	4	1880	20.0	<u>1840</u>	<u>20.4</u>	1820	20.7	4	1880	20.0	<u>1840</u>	<u>20.4</u>	1820	20.7		
444.namd	4	<u>516</u>	<u>62.2</u>	517	62.1	516	62.2	4	<u>516</u>	<u>62.2</u>	517	62.1	516	<u>62.2</u>		
447.dealII	4	<u>615</u>	<u>74.4</u>	614	74.5	618	74.0	4	<u>605</u>	<u>75.6</u>	603	75.9	611	74.9		
450.soplex	4	1360	24.5	1330	25.1	<u>1330</u>	<u>25.1</u>	4	1240	26.9	1220	27.3	<u>1220</u>	<u>27.3</u>		
453.povray	4	261	81.6	<u>259</u>	<u>82.2</u>	258	82.4	4	200	106	200	106	<u>200</u>	<u>106</u>		
454.calculix	4	571	57.8	578	57.1	<u>574</u>	<u>57.5</u>	4	569	58.0	560	59.0	<u>560</u>	<u>59.0</u>		
459.GemsFDTD	4	1920	22.1	1900	22.3	<u>1900</u>	<u>22.3</u>	4	1920	22.1	1900	22.3	<u>1900</u>	<u>22.3</u>		
465.tonto	4	881	44.7	<u>878</u>	<u>44.8</u>	877	44.9	4	837	47.0	<u>836</u>	<u>47.1</u>	833	47.3		
470.lbm	4	3430	16.0	<u>3360</u>	<u>16.4</u>	3340	16.5	4	3090	17.8	<u>3050</u>	<u>18.0</u>	3040	18.1		
481.wrf	4	1340	33.4	1330	33.5	<u>1330</u>	<u>33.5</u>	4	1340	33.4	1330	33.5	<u>1330</u>	<u>33.5</u>		
482.sphinx3	4	2210	35.3	2190	35.6	<u>2200</u>	<u>35.4</u>	4	2110	36.9	<u>2090</u>	<u>37.2</u>	2070	37.6		
Results appear in the order in which they were run. Bold underlined text indicates a median measurement.																
General Notes																
BIOS configuration: Hardware Prefetch - Disable, Adjacent Sector Prefetch - Disable																
Base Compiler Invocation																
C benchmarks: icc																
C++ benchmarks: icpc																
Fortran benchmarks: ifort																
Continued on next page																
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IBM Corporation IBM System x3650 Intel Xeon processor 5160, 3.0 GHz			SPECfp_rate2006 = 41.2		
			SPECfp_rate_base2006 = 39.8		
CPU2006 license #: 3184	Test sponsor: Tested by:	IBM Corporation Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007
Base Compiler Invocation (Continued)					
Benchmarks using both Fortran and C: ifort icc					
Peak Compiler Invocation					
C benchmarks: /opt/intel/cc/9.1.051/bin/icc -I/opt/intel/cc/9.1.051/include -L/opt/intel/cc/9.1.051/lib					
C++ benchmarks (except as noted below): icpc 450.soplex: /opt/intel/cc/9.1.051/bin/icpc -I/opt/intel/cc/9.1.051/include -L/opt/intel/cc/9.1.051/lib					
Fortran benchmarks: 434.zeusmp: /opt/intel/fc/9.1.051/bin/ifort -I/opt/intel/fc/9.1.051/include -L/opt/intel/fc/9.1.051/lib 465.tonto: ifort					
Benchmarks using both Fortran and C (except as noted below): ifort icc					
Base Portability Flags					
C benchmarks: -DSPEC_CPU_LP64					
C++ benchmarks (except as noted below): -DSPEC_CPU_LP64 453.povray: -DSPEC_CPU_LP64					
Fortran benchmarks: -DSPEC_CPU_LP64					
Benchmarks using both Fortran and C (except as noted below): -DSPEC_CPU_LP64 -nofor_main 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG					
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IBM Corporation IBM System x3650 Intel Xeon processor 5160, 3.0 GHz			SPECfp_rate2006 = 41.2		
			SPECfp_rate_base2006 = 39.8		
CPU2006 license #: 3184	Test sponsor: Tested by:	IBM Corporation Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007
Peak Portability Flags					
<p>C benchmarks: No flags used</p> <p>C++ benchmarks:</p> <ul style="list-style-type: none"> 447.dealII: -DSPEC_CPU_LP64 450.soplex: No flags used 453.povray: -DSPEC_CPU_LP64 <p>Fortran benchmarks:</p> <ul style="list-style-type: none"> 434.zeusmp: No flags used 465.tonto: -DSPEC_CPU_LP64 <p>Benchmarks using both Fortran and C (except as noted below):</p> <ul style="list-style-type: none"> -DSPEC_CPU_LP64 -nofor_main 					
Base Optimization Flags					
<p>C benchmarks: -fast</p> <p>C++ benchmarks: -fast</p> <p>Fortran benchmarks: -fast</p> <p>Benchmarks using both Fortran and C:</p> <ul style="list-style-type: none"> -fast 					
Peak Optimization Flags					
<p>C benchmarks (except as noted below):</p> <ul style="list-style-type: none"> -fast -prof_gen(pass 1) -prof_use(pass 2) 482.sphinx3: -fast <p>C++ benchmarks (except as noted below):</p> <ul style="list-style-type: none"> -fast -prof_gen(pass 1) -prof_use(pass 2) <p>Fortran benchmarks (except as noted below):</p> <ul style="list-style-type: none"> basepeak - yes 					
Continued on next page					
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IBM Corporation	SPECfp_rate2006 = 41.2				
IBM System x3650 Intel Xeon processor 5160, 3.0 GHz	SPECfp_rate_base2006 = 39.8				
CPU2006 license #: 3184	Test sponsor: Tested by:	IBM Corporation Principled Technologies	Test date: Sep-2007	Hardware Availability: Sep-2007	Software Availability: Sep-2007
Peak Optimization Flags (Continued)					
434.zeusmp: -fast 465.tonto: -fast -prof_gen(pass 1) -prof_use(pass 2) Benchmarks using both Fortran and C (except as noted below): -fast -prof_gen(pass 1) -prof_use(pass 2) 436.cactusADM: basepeak - yes 481.wrf: basepeak - yes					
Other Flags					
C benchmarks: No flags used C++ benchmarks: No flags used Fortran benchmarks: No flags used Benchmarks using both Fortran and C: No flags used					
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